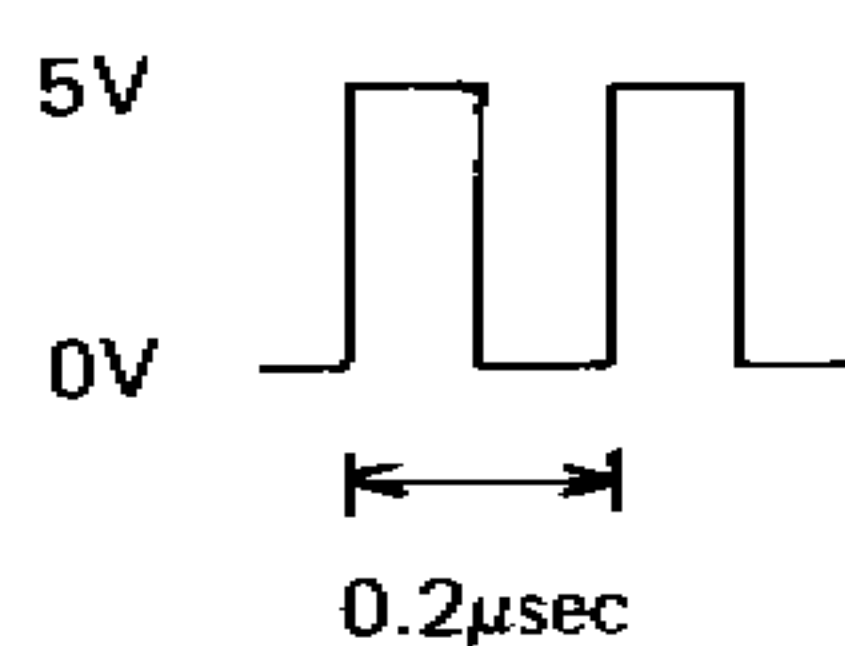
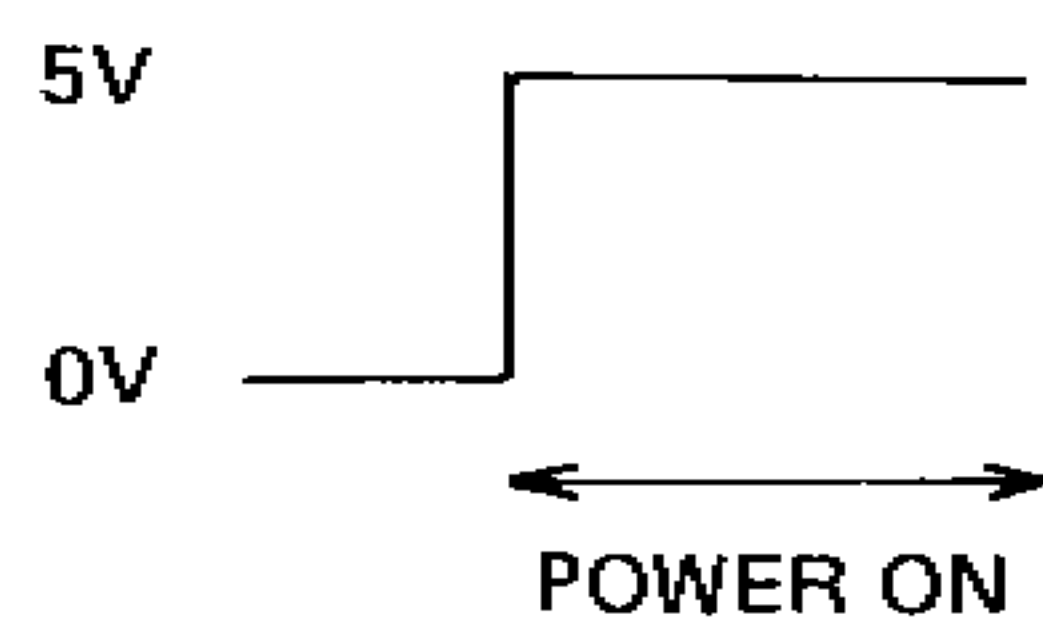


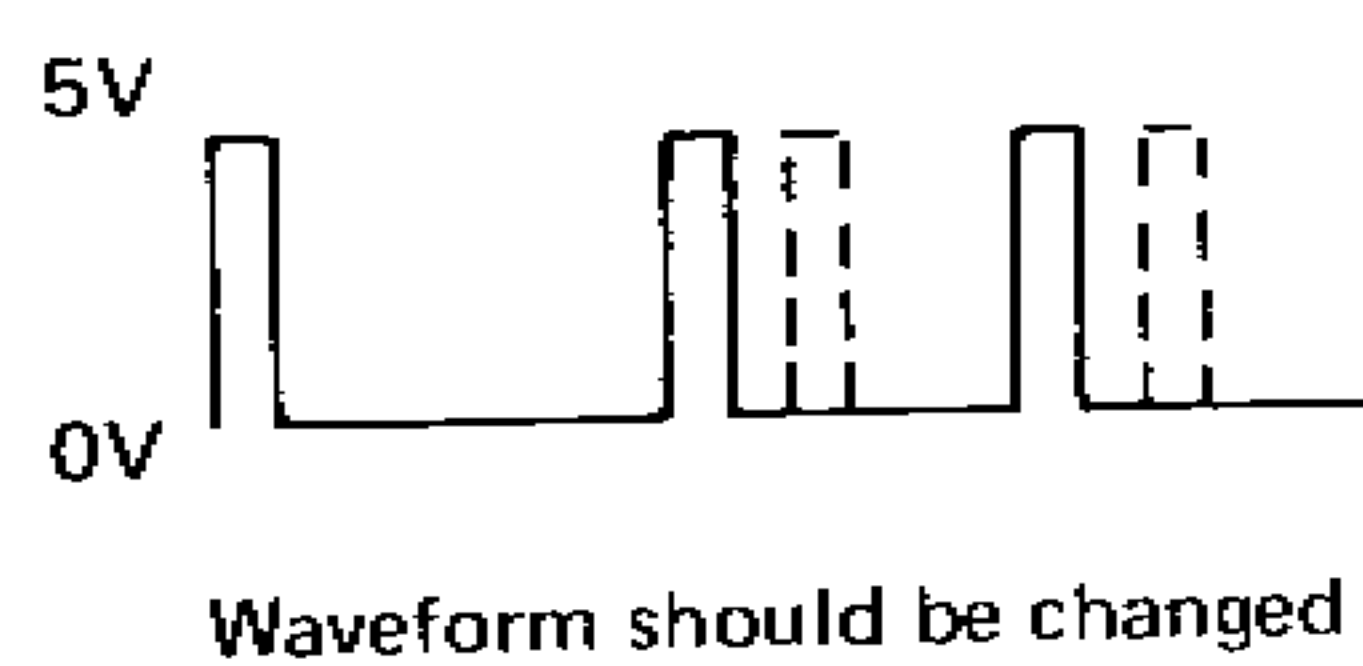
A CPU CLOCK



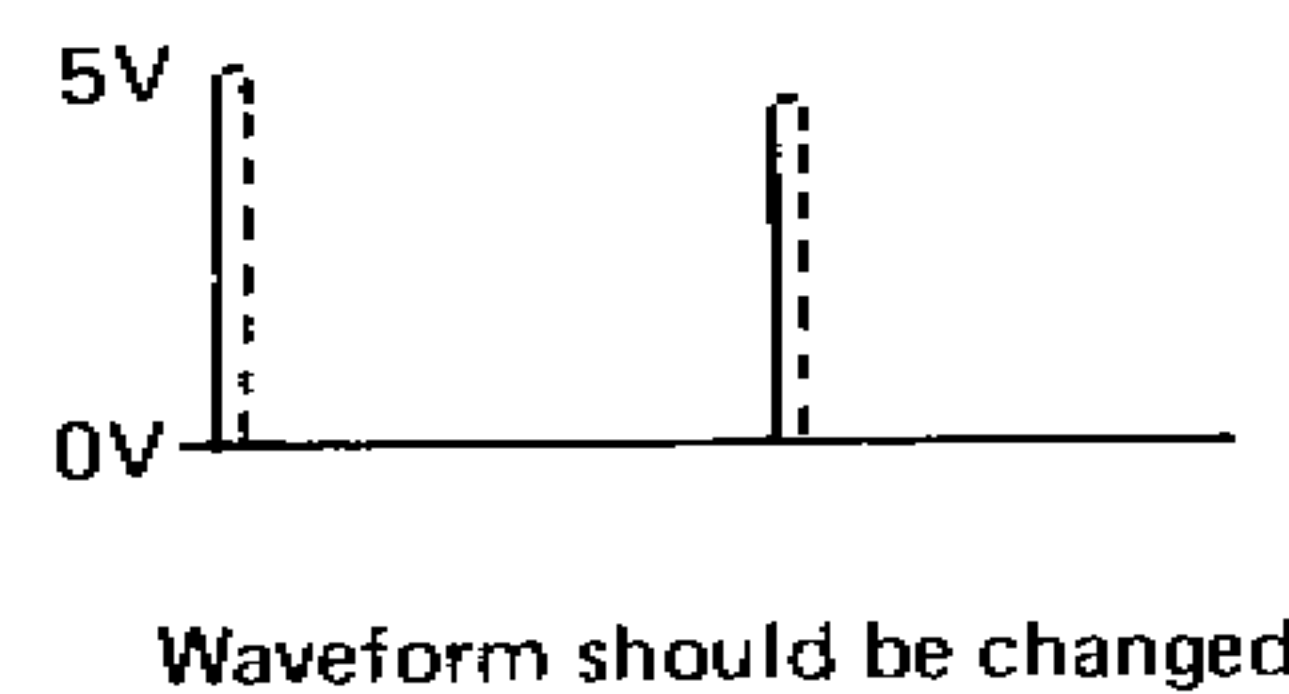
B CPU RESET



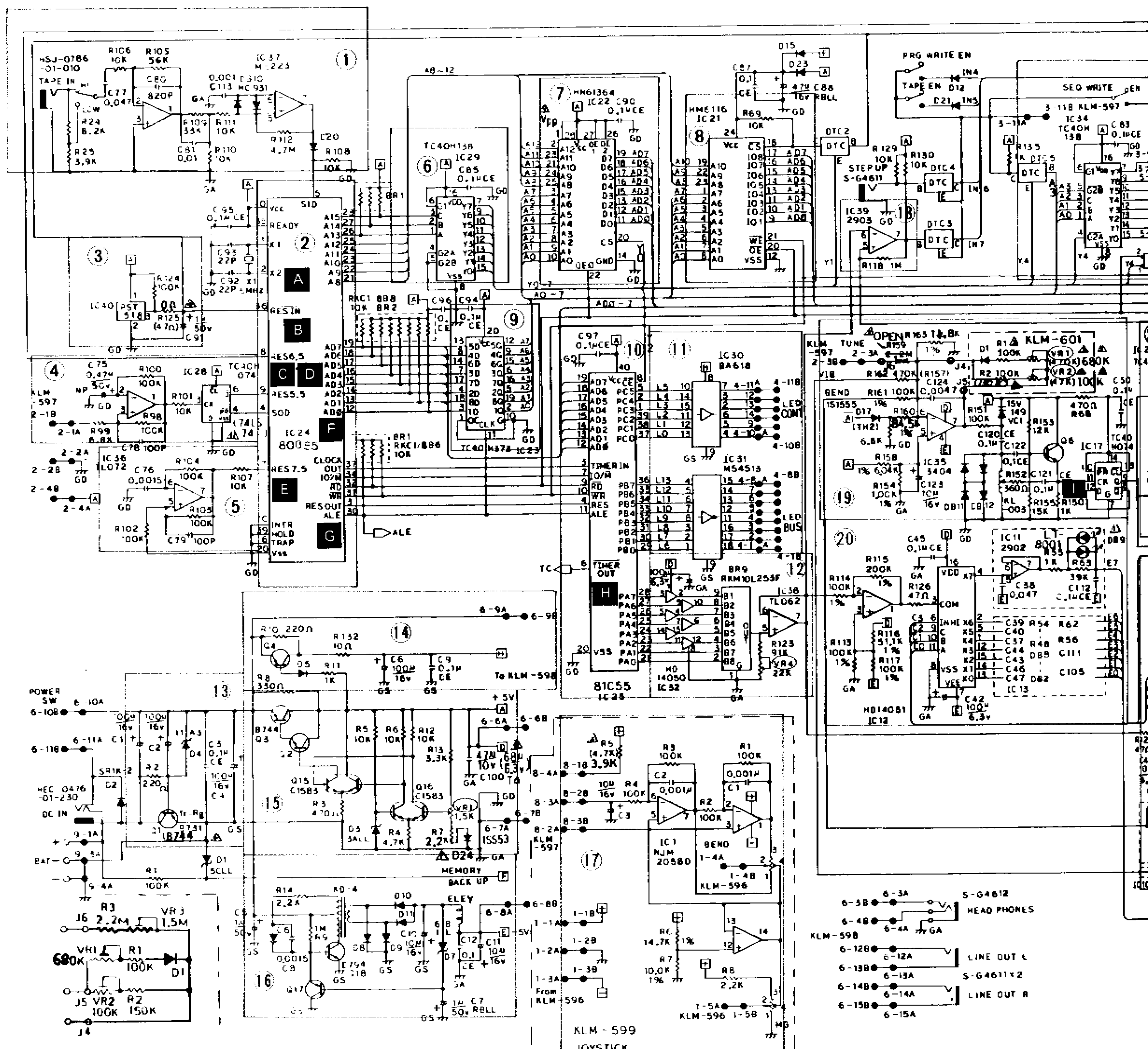
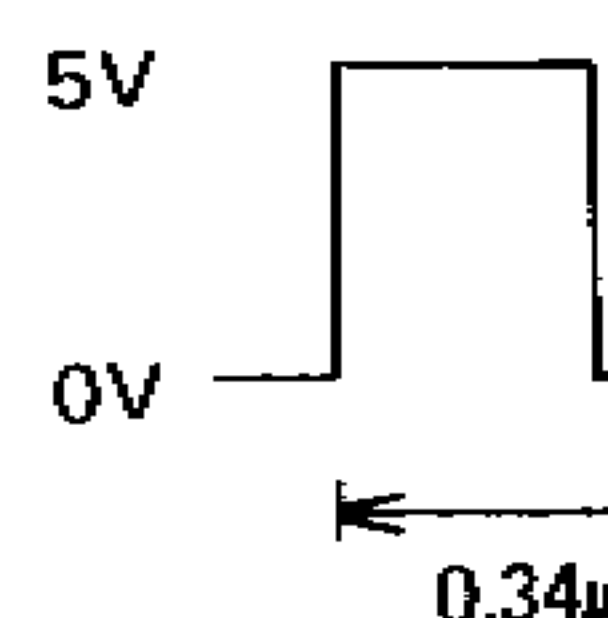
C CPU RES 6.5



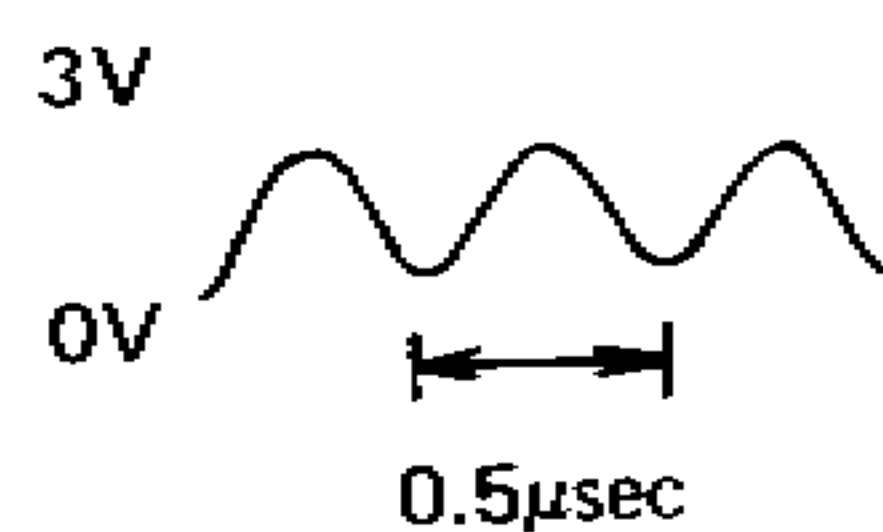
D CPU RES 5.5



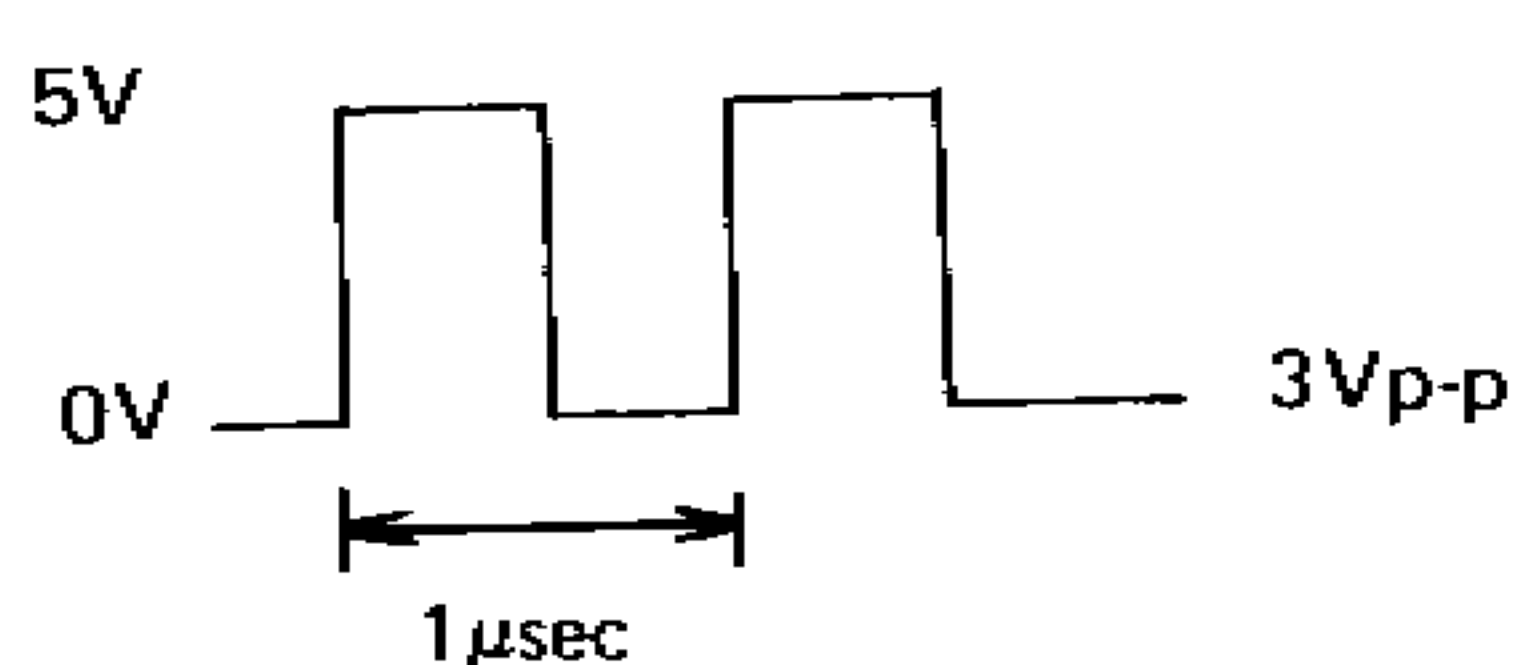
E CPU RES



I OSC CLOCK



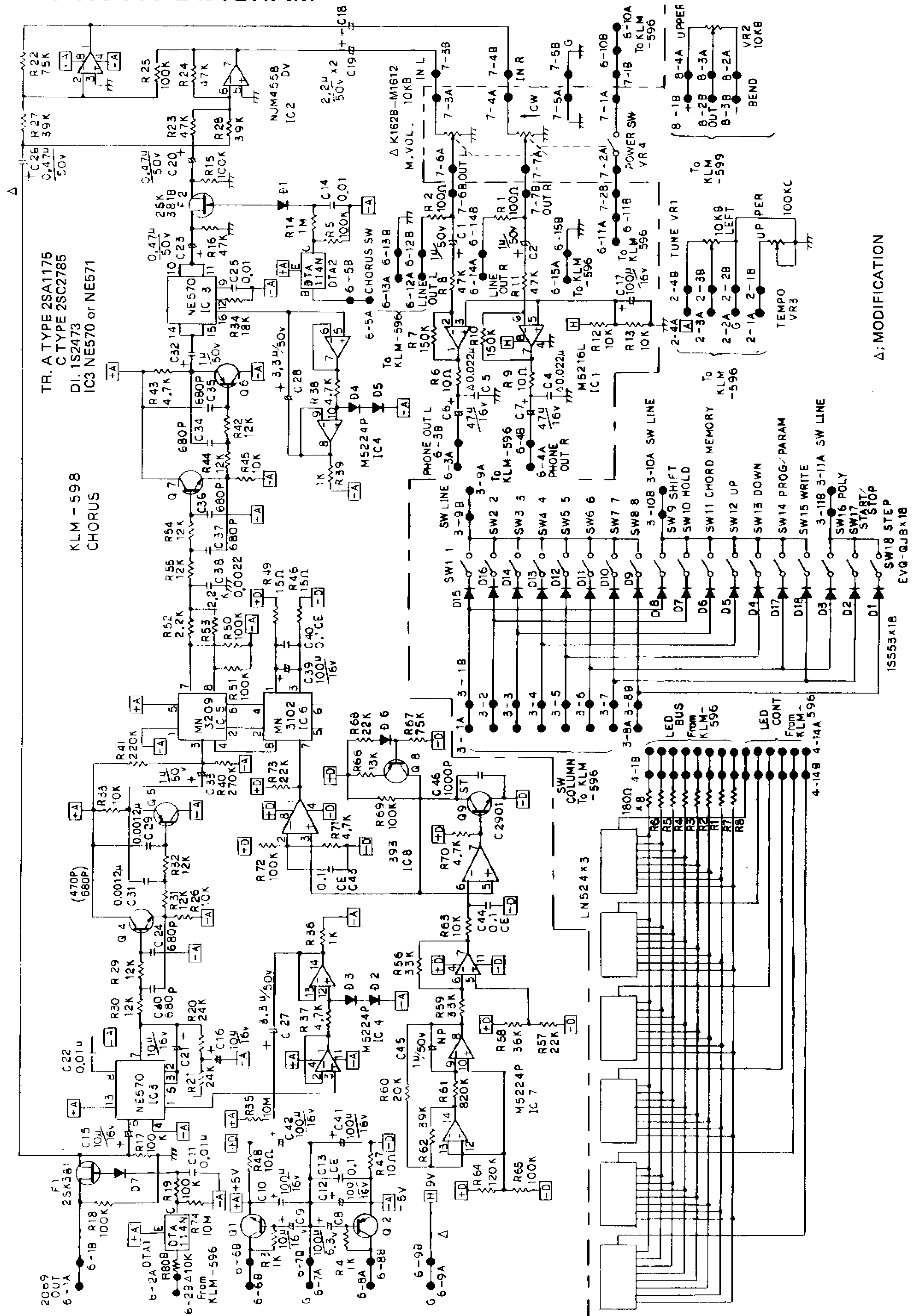
J **K** CLOCK1, 2



L WHITE NOISE



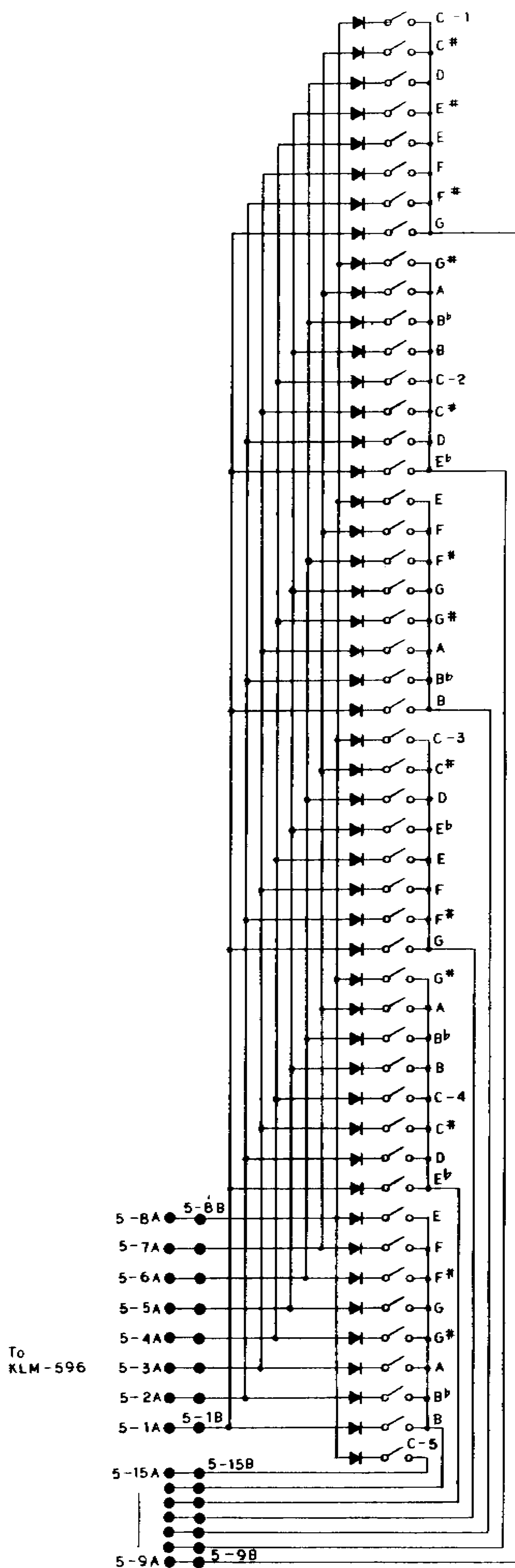
4. CIRCUIT DIAGRAM



Δ ; MODIFICATION

EVQ-QJBx18

KEY BOARD SWITCH MATRIX (49 Key)



CONNECTOR DIAGRAM

